**OFI WG Weekly telecom – 11/18/2014**

**Agenda Topics:**

* Brief update with Jim regarding the MWG
* Bernard Metzler walked through a high-level set of API requirements relative to accessing NVM storage. The details are described in the document bytes\_io\_reqs.pdf on the OFIWG download page.

**OFIWG Download Site:** [www.openfabrics.org](http://www.openfabrics.org)/downloads/OFIWG

Jim indicated that he has the information that he needs, and Bill Lee from the marketing working group is defining press release documentation to align with OFI release 1.0 near the end of the year.

Bernard provided a high-level view of storage requirements. He was originally going to cover this in the data storage and data access sub-group, but had a mix-up with the meeting invite. There were several features which were discussed, roughly divided into these areas:

* Support for general message data transfer and RMA operations. These requirements align with current OFI interfaces.
* Work request execution and completion ordering. NVM can be more efficient if both data processing order at the target and completion ordering at the source can be relaxed. There was also a new request to allow the ‘chaining’ or ‘fusing’ of operations, such that requests further down the chain are only initiated if prior requests complete successfully.
* Write completion level. This requirement aligns with current OFI interfaces and are similar to requirements provided to OFIWG earlier.
* Read and write ahead acceleration. Conceptually this is the ability of the transport to carry notification hints about future requests that the target may receive.
* Memory registration. Support for both zero based addressing and mapped VA addresses. And support for persistent memory regions/objects.
* Application level command support – specifically, the NVM TRIM command.
* Support for mixing DRAM and NVM within a single work request.

Additional lower-level details will also be provided in future data storage and data access meetings.