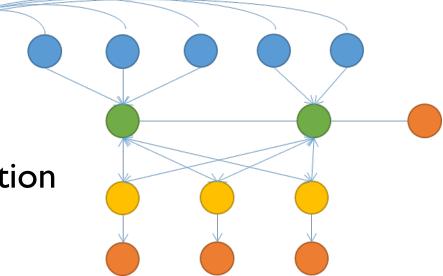


Utilizing HPC Network Technologies in High Energy Physics Experiments



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On behalf of the ATLAS Collaboration





25th Annual Symposium on High-Performance Interconnects Santa Clara, 2017

High Energy Physics and Interconnects

In this presentation we will talk about...

... why High Energy Physics (HEP) needs HPC Interconnects

... why the current state-of-the art HPC network APIs are not useful for HEP

... what we can **do about it** (spoiler: we build our own API)

High Energy Physics

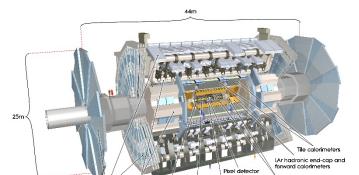


Large Hadron Collider (LHC) 27km circular collider in Geneva, Switzerland



Particle colliders used in HEP study physics processes on a microscopic scale

	Length (m)	46	
	Diameter (m)	25	
	Weight (t)	7000	
	Number of electronic channels	100.10^{6}	
E	<text></text>	ctromagnetic calorimeters	o and



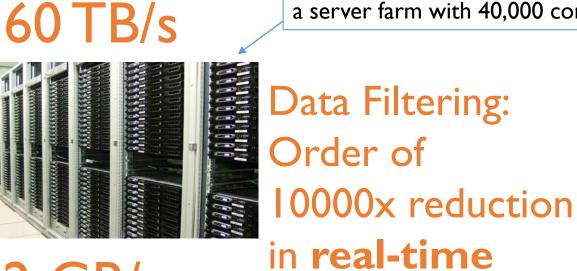
lar electromagnetic calorimeters

Transition radiation tracke

Semiconductor tracke

Data Acquisition

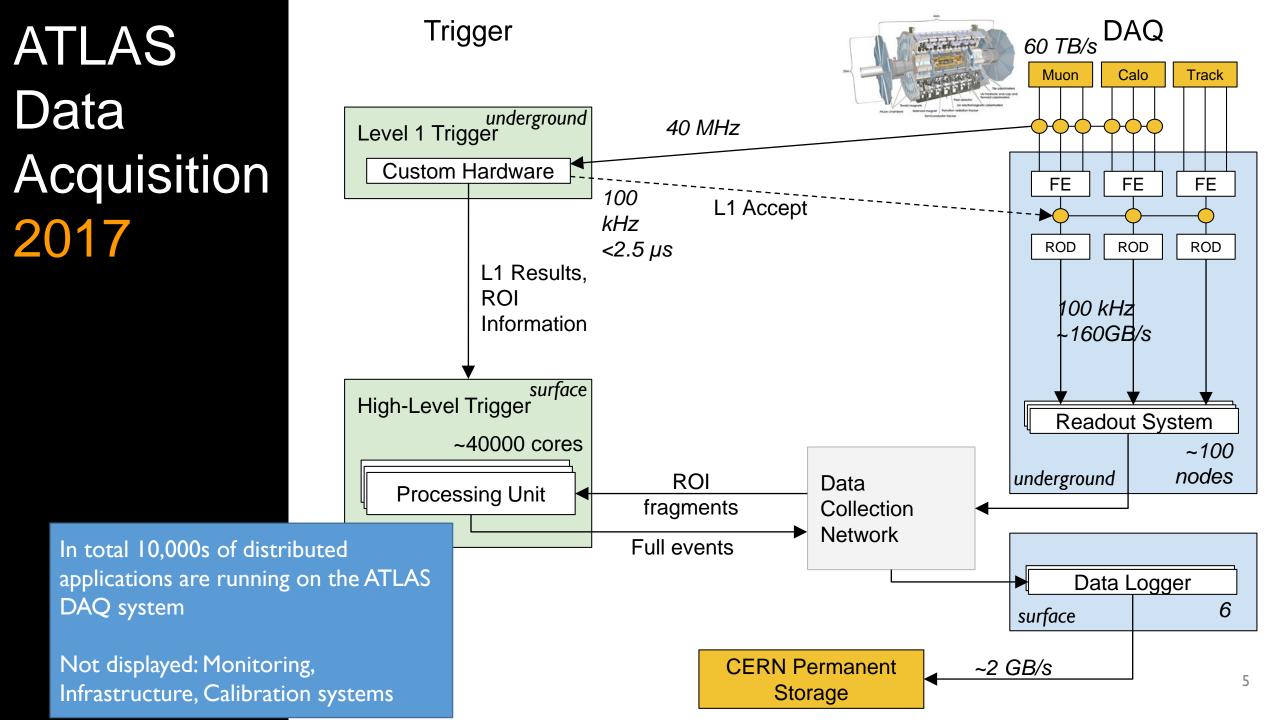
Custom electronics and a server farm with 40,000 cores



Needle in a haystack: Looking for extremely rare events with a probability of 10⁻¹³

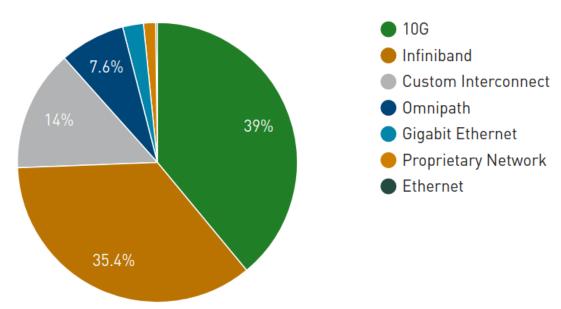
2 GB/s

Need **High Performance Networks** to move data at high rates under real-time conditions



High Performance Networks

Interconnect Families in Top500 List in July 2017

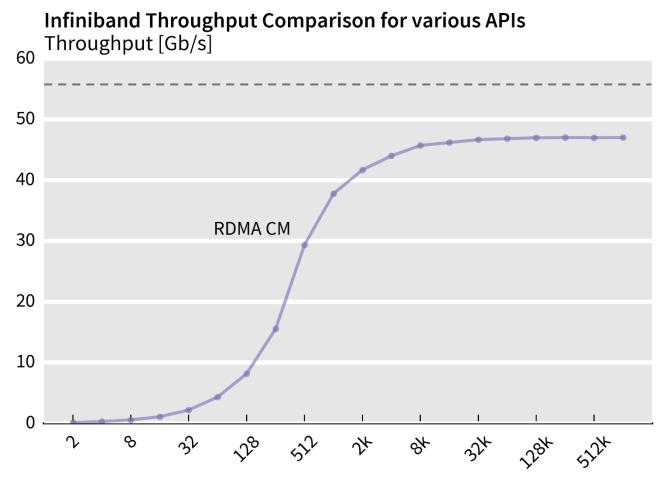


Ethernet and **Infiniband** are the two dominant technologies in the HPC market, **OmniPath** gaining share

ATLAS, ALICE, CMS and LHCb – the four LHC experiments – all use Ethernet and/or Infiniband in their DAQ systems

Source: top500.org

What API to use for HPC networks in the HEP environment? Comparison of APIs based on performance and suitability for HEP



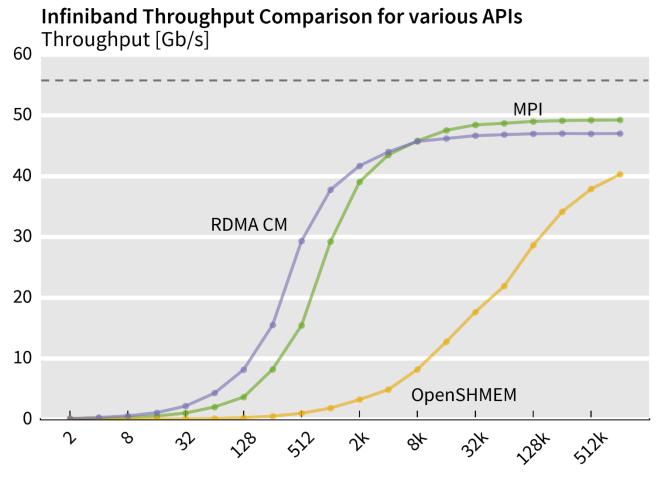
56G Infiniband FDR

Data-transfer between two nodes (Intel Haswell, 8-core and 10-core systems)

Connected via a single switch

Native APIs like Verbs or RDMA CM: Good performance but cumbersome to use and no high-level patterns

Message Size [Byte]



56G Infiniband FDR

Data-transfer between two nodes (Intel Haswell, 8-core and 10-core systems)

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HPC APIs like MPI or PGAS/OpenSHMEM: Good performance (MPI), but paradigm does not fit the HEP use case

Message Size [Byte]

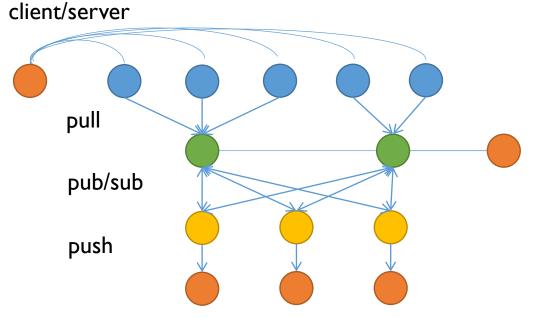
Server clusters in...

HPC

HEP

message passing

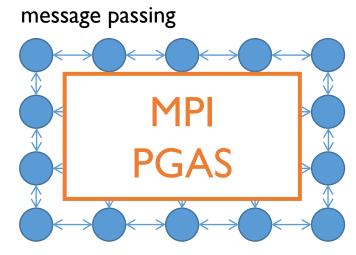
Regular topology SPMD Pattern No Real-Time Requirements No Failure Tolerance Static Resource Management



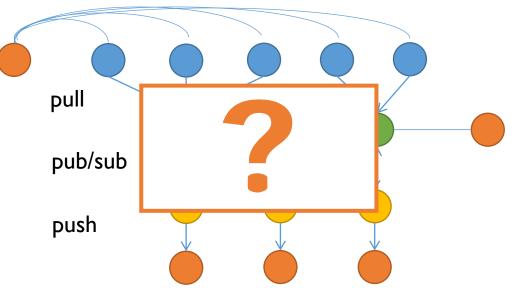
Complex topology Complex distributed system Real-Time Requirements Some Failure Tolerance Dynamic Resource Management Server clusters in...

HPC

HEP

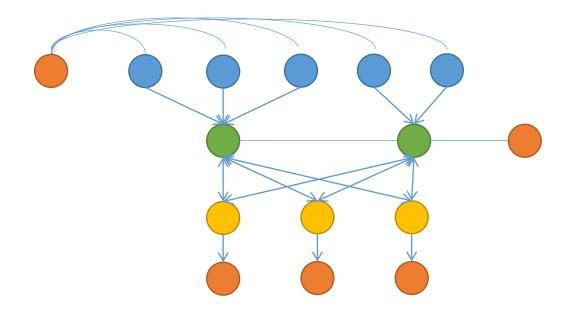


Regular topology SPMD Pattern No Real-Time Requirements No Failure Tolerance Static Resource Management client/server



Complex topology Complex distributed system Real-Time Requirements Some Failure Tolerance Dynamic Resource Management

Requirements



High Throughput (ATLAS Data Acquisition system has to transport more than 100 GB/s)

Low Latency connections for detector control and calibration applications

High level communication patterns like client/server and publish/subscribe

Technology agnostic

Closest match for an API satisfying HEP requirements: 2000



High level communication patterns like publish/subscribe, client/server, push/pull

Simple, clean API

Can we use ØMQ for HEP purposes? It is already in use, see

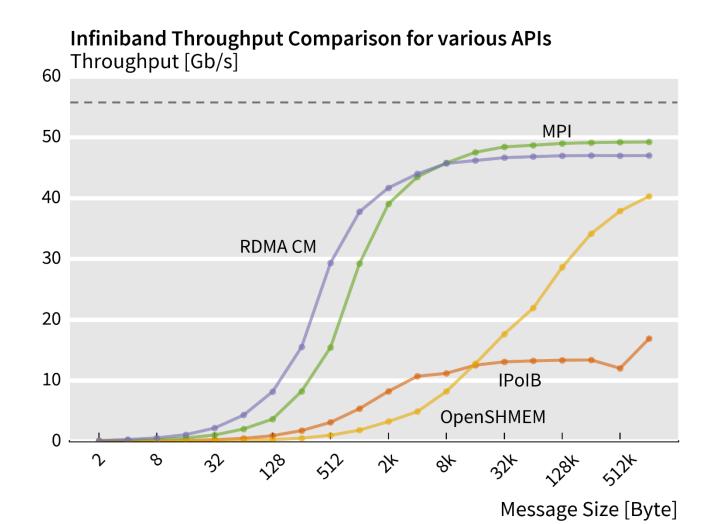
Middleware trends and market leaders 2011 A. Dworak, F. Ehm, W. Sliwinski, M. Sobczak, CERN, Geneva, Switzerland

But how does it hold up in a data acquisition context?

NO native support for HPC interconnects

NO high-throughput mode

Tuned for low-latency



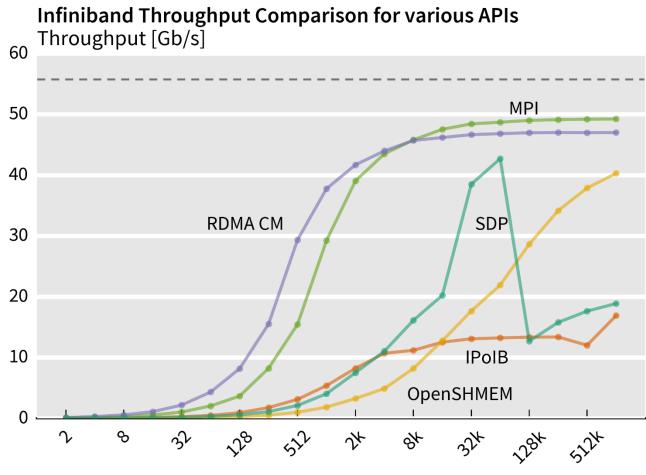
ØMQ on Infiniband needs to be run on an emulation layer IPoIB or SDP

56G Infiniband FDR

Data-transfer between two nodes (Intel Haswell, 8-core and 10-core systems)

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IPoIB: Low performance



ØMQ on Infiniband needs to be run on an emulation layer IPoIB or SDP

56G Infiniband FDR

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Connected via a single switch

Socket Direct Protocol: Better than IPoIB, but not nearly as good as native APIs

Infiniband Throughput Comparison for various APIs Throughput [Gb/s] 60 MPI 50 40 **RDMA CM** SDP 30 20 10 **IPolB** OpenSHMEM 0 3r $\sqrt{2}^{8}$ 228× \$ 15 Ъ 324 5124

Message Size [Byte]

ØMQ on Infiniband needs to be run on an emulation layer IPoIB or SDP

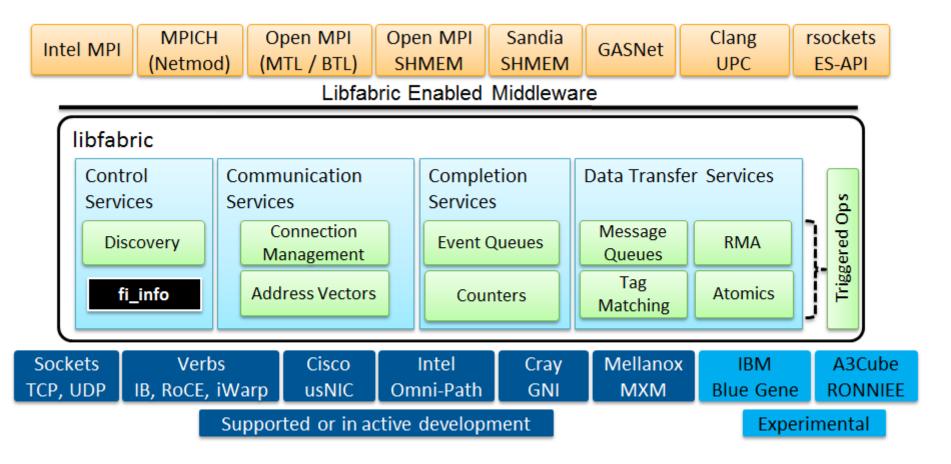
56G Infiniband FDR

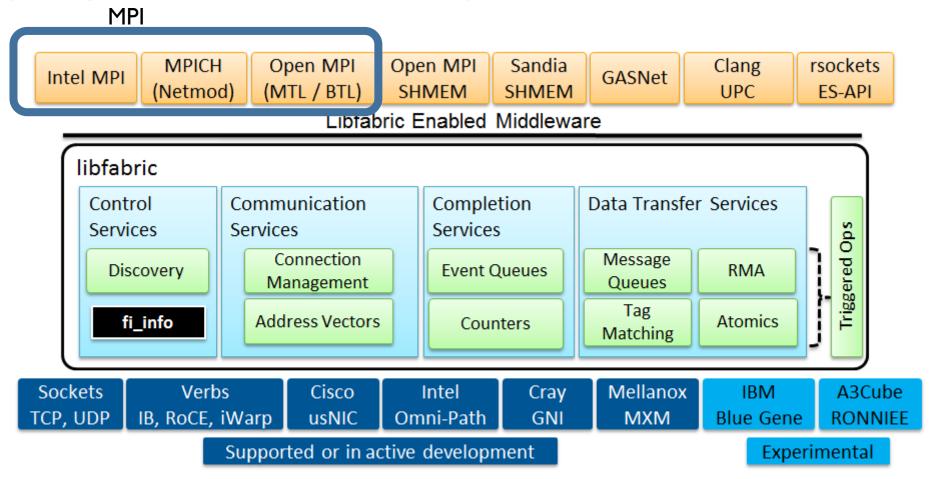
Data-transfer between two nodes (Intel Haswell, 8-core and 10-core systems)

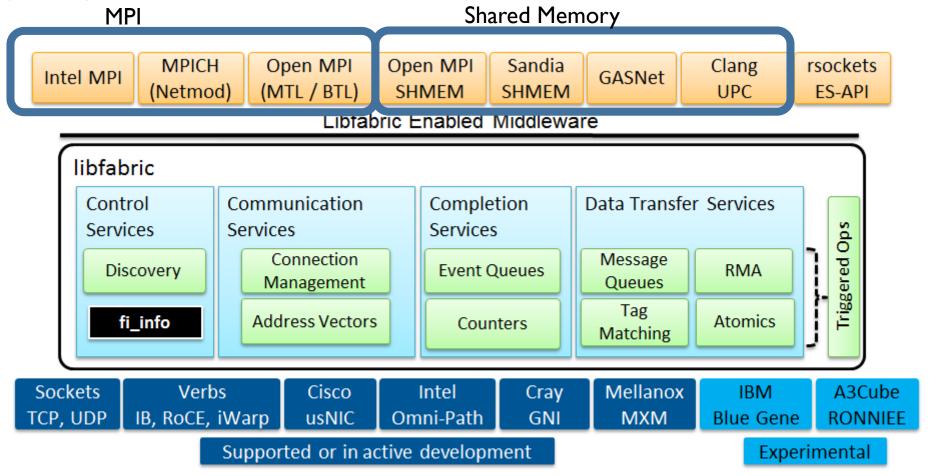
Connected via a single switch

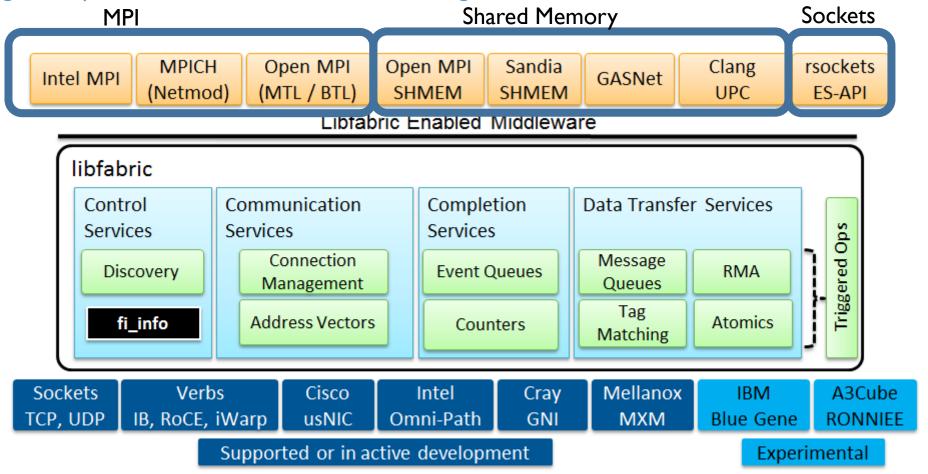
Emulation layers have a significant performance penalty

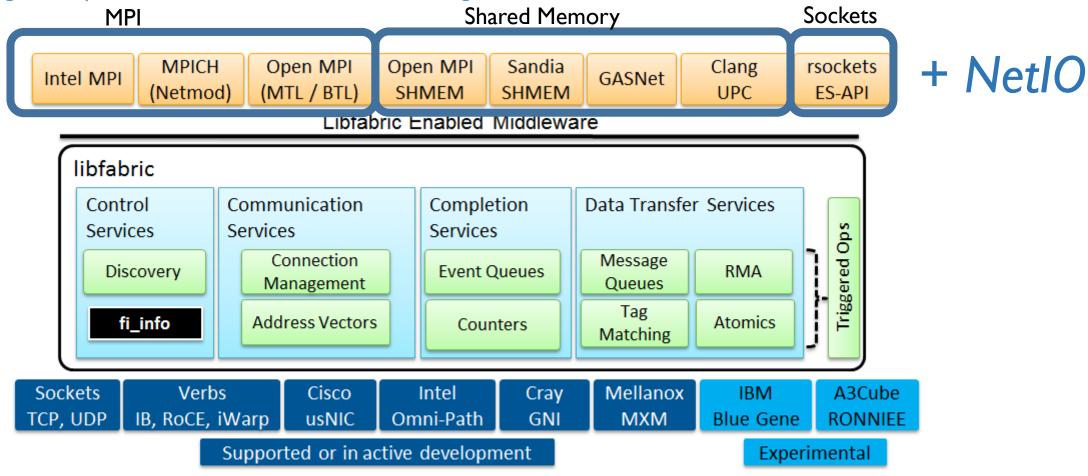












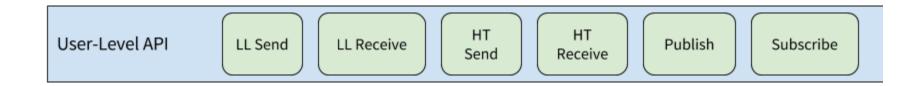


A high-level, general-purpose API for HPC networks

NetIO was designed with High Energy Physics experiments in mind, but it is not restricted to this use case

Design Goals:

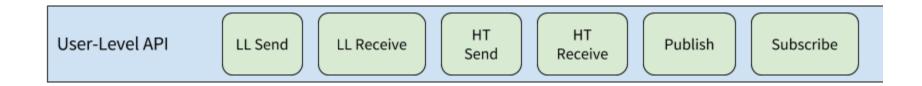
- Native support for HPC interconnects via a back-end system
- Different operation modes tuned for high-throughput communication or low-latency communication
- High-level communication patterns including publish/subscribe

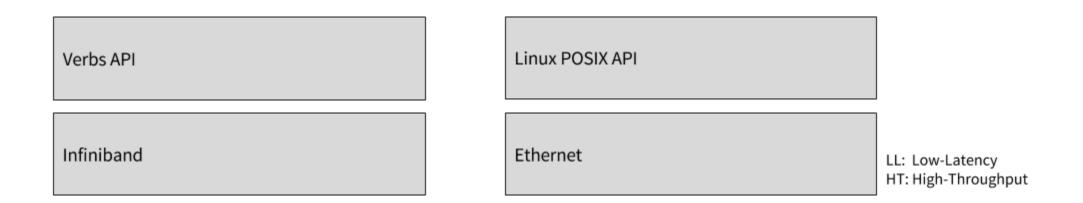


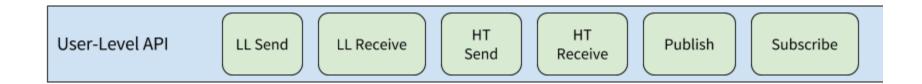
Infiniband

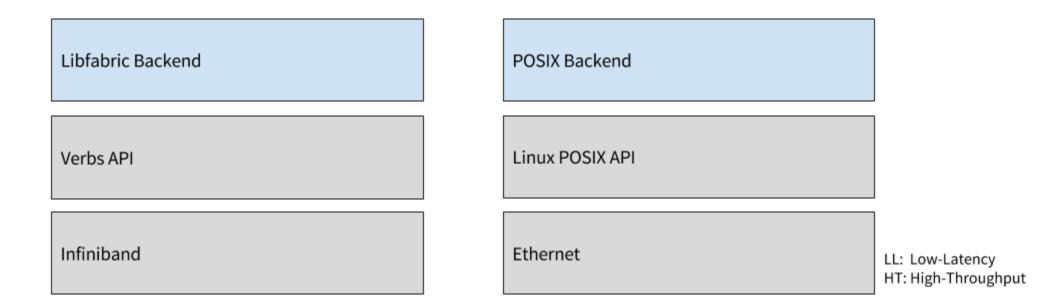
Ethernet

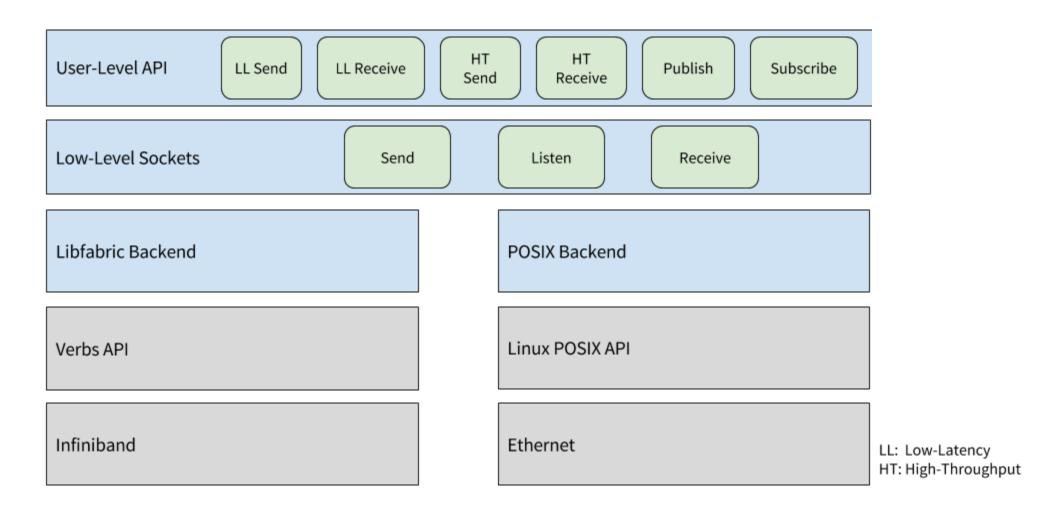
LL: Low-Latency HT: High-Throughput

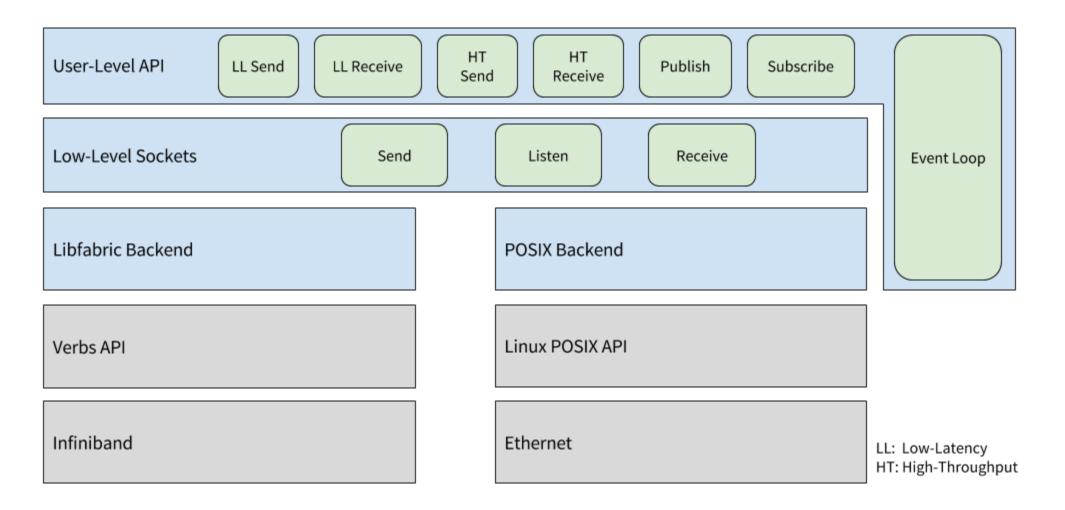












Memory Management

Messages are packed into **pages** (buffering for higher efficiency)

Typical max. page size is 1 MB

Event loop drives a timeout to send out partial pages and avoid connection starvatation

NetIO maintains a list of pre-allocated, free pages per connection Default: up to 256 pages per connection Pages are recycled after having been processed (i.e. fully sent or received)

Low-level sockets

Uniform interface used by user-level sockets

Abstract interface that is implemented by back-ends

Pages: Buffers that contain one or multiple messages

Listen Sockets: Listen to incoming connection requests, create receive sockets

Receive Sockets: Receive pages from remote endpoints, deserialize into messages

Send Sockets: Send pages to remote endpoint.

No distinction between high-throughput and low-latency communication (this is done in the user-level sockets)

Configuration interface to enable fine-tuning of connection parameters

User-level sockets

Provide a simple interface for users

High-level communication patterns:

- Send/Receive
- Publish/Subscribe

Come in two flavors:

- Low-latency
- High-Throughput

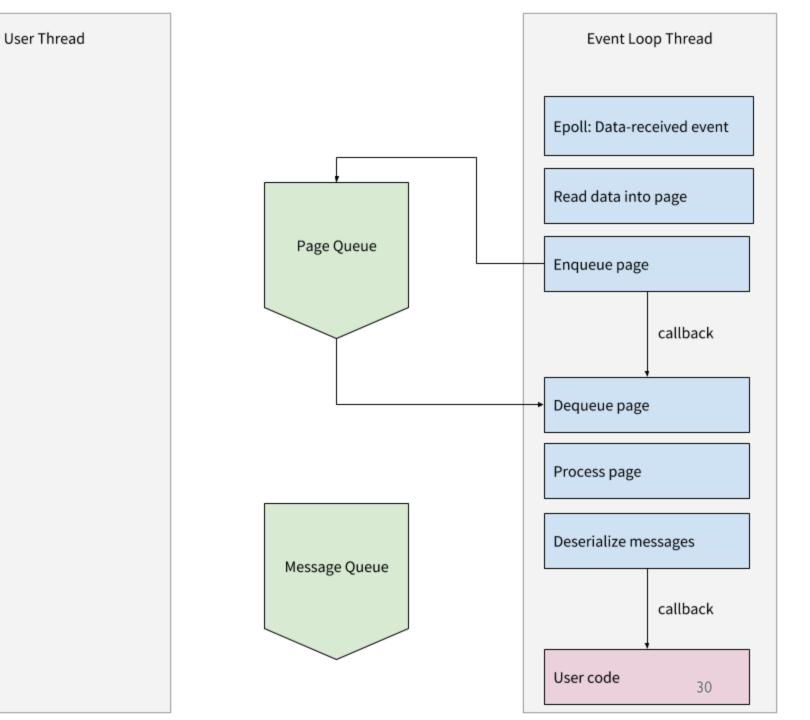
Addressing based on IPv4 or IPv6

Low-Latency Callback-based No buffering 	High-Throughput Queue-based Buffering 		
LL Send socket	HT Send socket		
LL Receive socket	HT Receive socket		
LL Subscribe socket	HT Subscribe socket		
Publish socket Both high-throughput and low-latency subscribe sockets can connect			

Low Latency Mode

Low latency

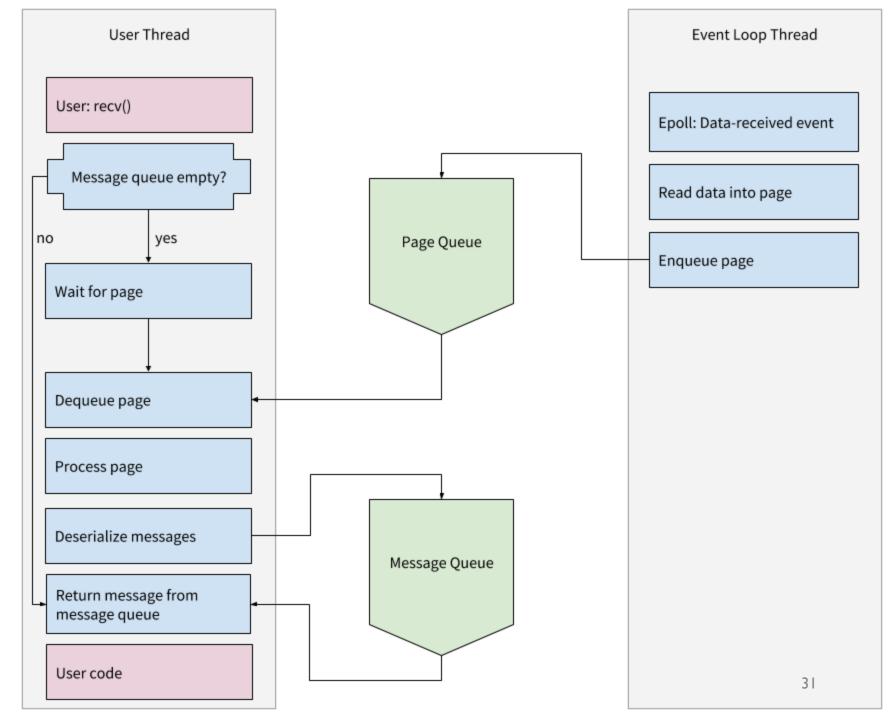
- No thread synchronization
- No buffering, pages contain a single message
- Skipping message queue
- Immediate handling of messages via callbacks
- In the future: also skip page queue



High Throughput Mode

High Throughput

- Minimal work in the event loop so it can return to process incoming pages
- Buffering: Multiple messages per page
- Event-loop drives buffer timeout to avoid connection starvation
- Explicit user call to retrieve messages



Back-ends

POSIX

Uses POSIX stream sockets (TCP), which translates naturally into the low-level socket API

Nagle's algorithm disabled (buffering in user-level sockets)

Simple integration with epoll event loop

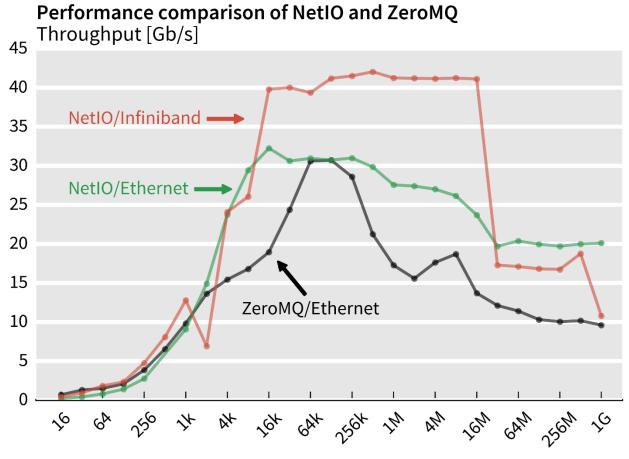
Libfabric

Uses libfabric Reliable Connection (RC)

RDM mode is not supported – ordering is needed to ensure proper deserialization (That means currently RDM-based libfabric providers are not supported, for example the PSM provider for OmniPath. OmniPath is instead supported by the Verbs provider) Send windows used to control data-flow for higher throughput

Uses file descriptors for asynchronous completion management – can be integrated in the epoll event loop

NetIO Throughput: Push/Pull



Message size [Byte]

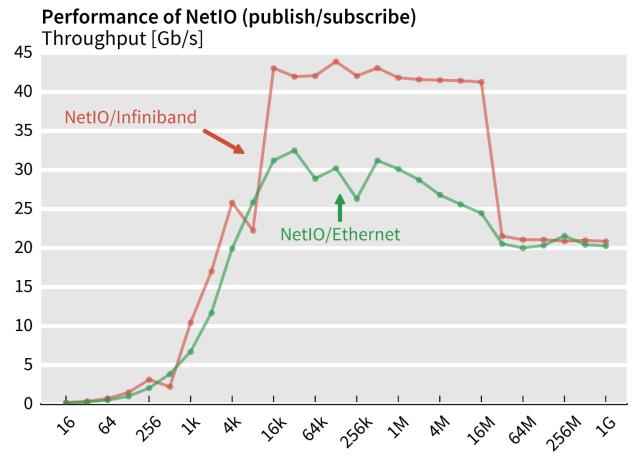
Push/Pull benchmarks

56G FDR Infiniband40G EthernetI MB pagesize

NetIO outperforms ZeroMQ in nearly all uses cases

Using the Infiniband mode of the underlying hardware allows a performance boost that we can leverage with NetlO – without changing our software

NetIO Throughput: Publish/Subscribe



Publish/Subscribe benchmarks

56G FDR Infiniband40G EthernetI MB pagesize

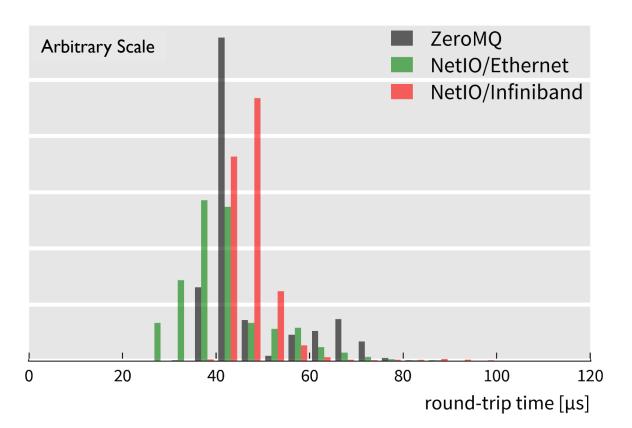
Similar to the push/pull benchmarks, using the Infiniband mode of the hardware yields a performance boost

ZeroMQ already discarded due to limited performance

Message Size [Byte]

NetIO Latency

Round-Trip Time (RTT) Comparison



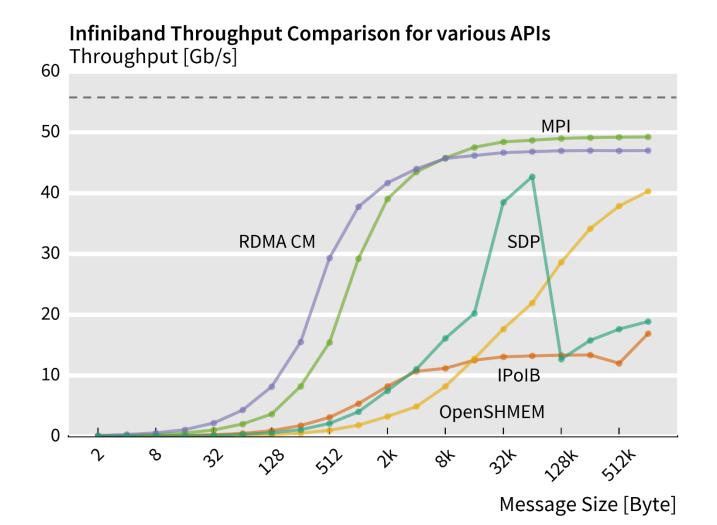
Point-to-Point benchmarks No additional load on switch

56G FDR Infiniband 40G Ethernet

Latency is very similar for ZeroMQ and NetIO.

Lower latency expected for NetIO/Infiniband: room for improvement

NetIO compared to other Infiniband APIs



56G Infiniband FDR

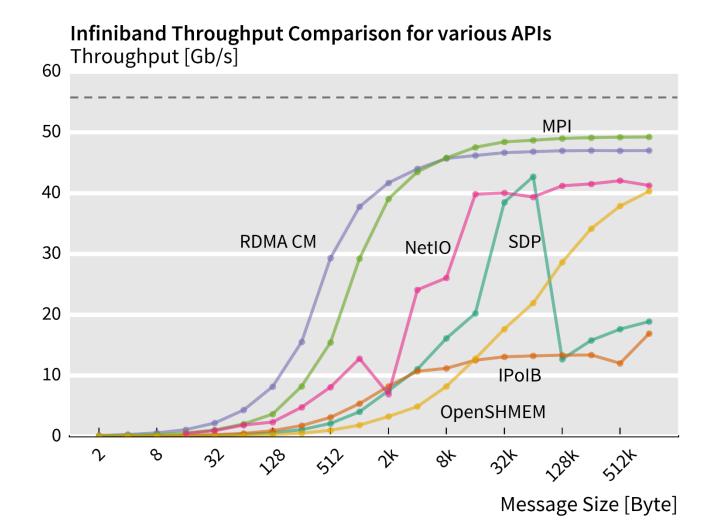
Data-transfer between two nodes (Intel Haswell, 8-core and 10-core systems)

Connected via a single switch

NetIO performance exceeds the performance of emulation layers

Still some room for improvement compared to MPI/native APIs

NetIO compared to other Infiniband APIs



56G Infiniband FDR

Data-transfer between two nodes (Intel Haswell, 8-core and 10-core systems)

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Still some room for improvement compared to MPI/native APIs

NetIO Status & Outlook

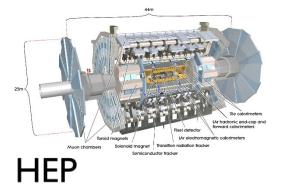
Some performance improvements planned

- New ZeroCopy mode
- Improved queuing scheme

Status

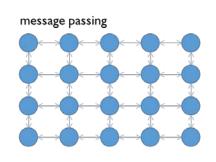
- Small functional improvements needed
- Ongoing parameter studies
- User documentation being written
- OpenSource release planned this year
- NetIO going to be used in ATLAS data-taking beginning 2019

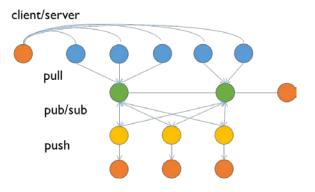
Conclusion

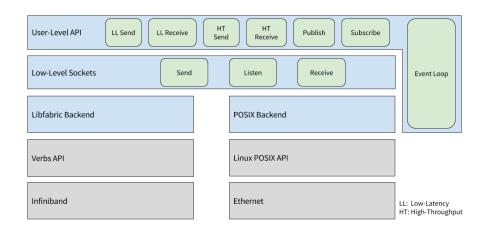


HPC interconnects are interesting technologies for HEP

HPC is fundamentally different from HEP computing and different network APIs are required

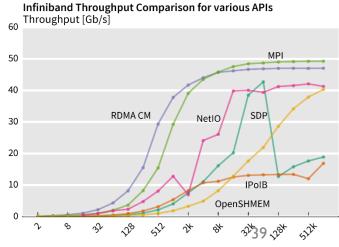






NetIO is network API with highlevel communication patterns and native support for Ethernet and HPC interconnects via a pluggable back-end system

To be used in ATLAS readout from 2019 on



HPC